

IN THE SPECIFICATION

Please replace the paragraph at page 4, lines 16-27, with the following rewritten paragraph:

In a case of a CD-ROM reproducing apparatus, in a standard for a disk, a deviation between the center of the track and a center of the a loading hole of the disk, namely the eccentricity caused by the manufacturing error of the disk, is defined as 70 μm at most. In addition to this eccentricity due to the disk manufacturing error, eccentricity such as a mechanical mounting error ~~defended~~ between the disk center and the rotation center of the disk when the disk is loaded on the spindle motor may occur, so that a total amount of eccentricity of about 200 μm may occur in an actual CD-ROM reproducing apparatus.

Please insert the following heading at page 7, between lines 26 and 27:

SUMMARY OF THE INVENTION

Please delete the heading at page 8, line 8, in its entirety.

Please replace the paragraph at page 19, line 26 to page 20, line 5, with the following rewritten paragraph:

It should be noted that the tracking control circuit 15, the reading-out speed detecting circuit 18, the band-pass filter 19 and the tracking correcting circuit 20 enclosed by a broken line in FIG. 4 may be formed in one semiconductor integrated circuit (IC). Of ~~cause~~ course, these circuits may be formed as discrete circuits.

Please replace the paragraph at page 22, line 20 to page 23, line 7, with the following rewritten paragraph:

4
A4
In the PLL circuit shown in FIG. 6, when the information signals 17 from the head amplifier 13 ~~[[is]]~~ are input in a phase comparing circuit 30 as a first input, unnecessary frequency components other than the phase error signal are removed from the output of the phase comparing circuit 30 by the filter circuit 31, and the output ~~[[is]]~~ of the filter circuit 31 is obtained as a PLL control voltage 32. The PLL control voltage 32 is output input into a voltage controlled oscillating circuit 33, so that PLL clock signal 34 whose frequency changes in response to the PLL control voltage 32 is output. The PLL clock signal 34 is input into the phase comparing circuit 30 as a second input, and the phase comparing circuit 30 compares the phases of the information signal 17 and the PLL clock signal 34 with each other.

Please replace the paragraph at page 32, lines 7-13, with the following rewritten paragraph:

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A5
When the tracking servo is in the off state, the selecting circuit 42 is switched to the storage circuit 41 side, and then a tracking operation of the disk 11 is ~~performed~~ performed by a synthesized signal of the output of the storage circuit 41 and the output of the tracking control circuit 15. Then, the control proceeds to step S13.

Please replace the paragraph at page 33, line 22 to page 34, line 11, with the following rewritten paragraph:

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A6
Thus, since the amount of eccentricity is determined on the basis of ~~[[an]]~~ a manufacturing error of the disk 11 itself and a mounting error occurring when the disk 11 is mounted to the spindle motor 16, the amount of eccentricity does not change in general once

the disk 11 is mounted to the spindle motor 16. The influence on the tracking control due to the eccentricity is repeated for each one rotation of the disk 11. Therefore, by storing the amount of eccentricity corresponding to one rotation of the disk, even when the reading-out speed can not be detected, the amount of eccentricity which is stored in the storage circuit 41 can compensate for such a failure in detection. Accordingly, once the amount of eccentricity corresponding to one rotation is stored, the tracking control can be performed using the amount of eccentricity which is stored thereafter.
